



IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2816  
Examiner: To be assigned

In Re PATENT APPLICATION OF:

Applicant(s) : Hiroki GOKO

Serial No. : 10/649,931

Filed : August 28, 2003

For : SEMICONDUCTOR INTEGRATED  
CIRCUIT

Attorney Ref: OKI 369

**STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

*Steven M. Rabin*

May 31, 2004

Date

SMR:klc

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